

FIG. 1 (PRIOR ART)

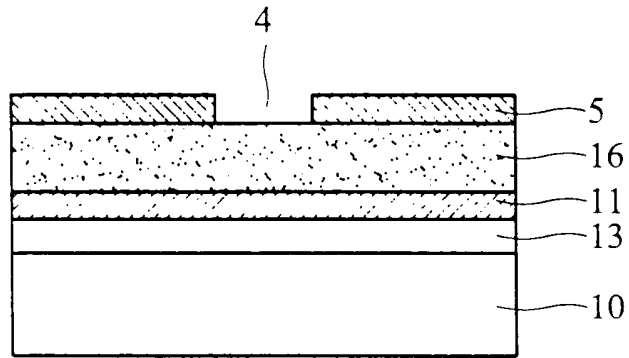


FIG. 2a (PRIOR ART)

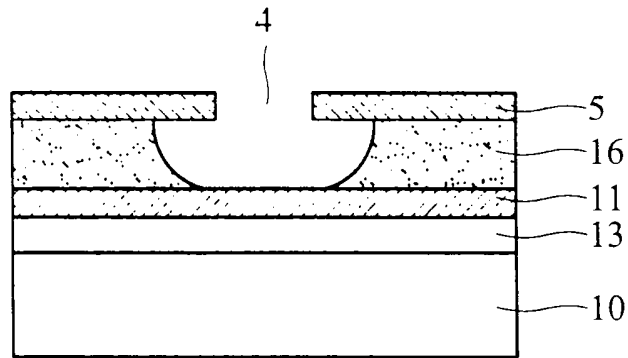


FIG. 2b (PRIOR ART)

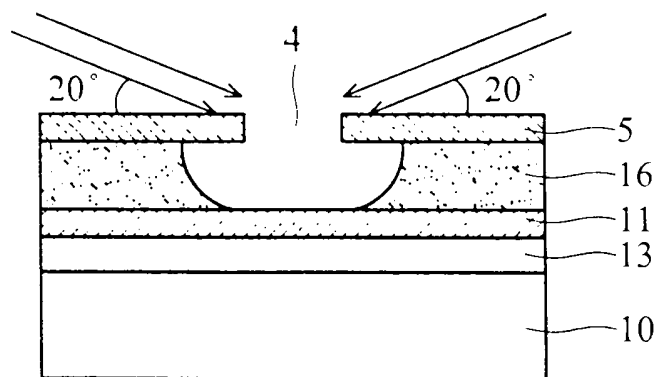


FIG. 2c (PRIOR ART)

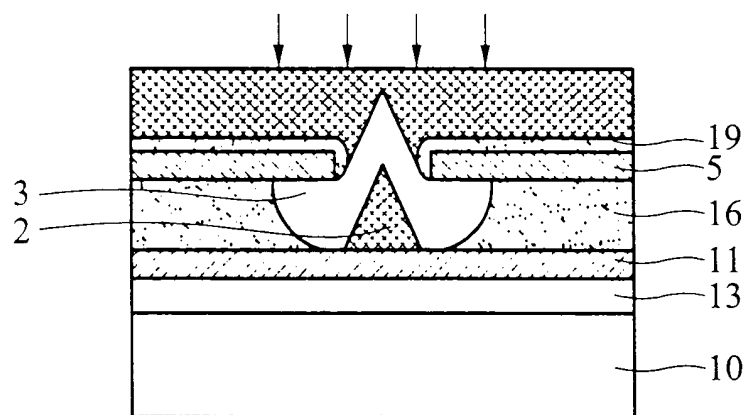


FIG. 2d (PRIOR ART)

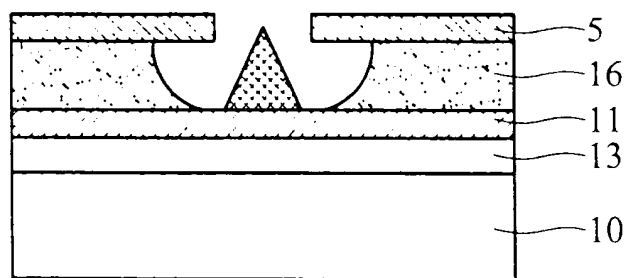


FIG. 2e (PRIOR ART)

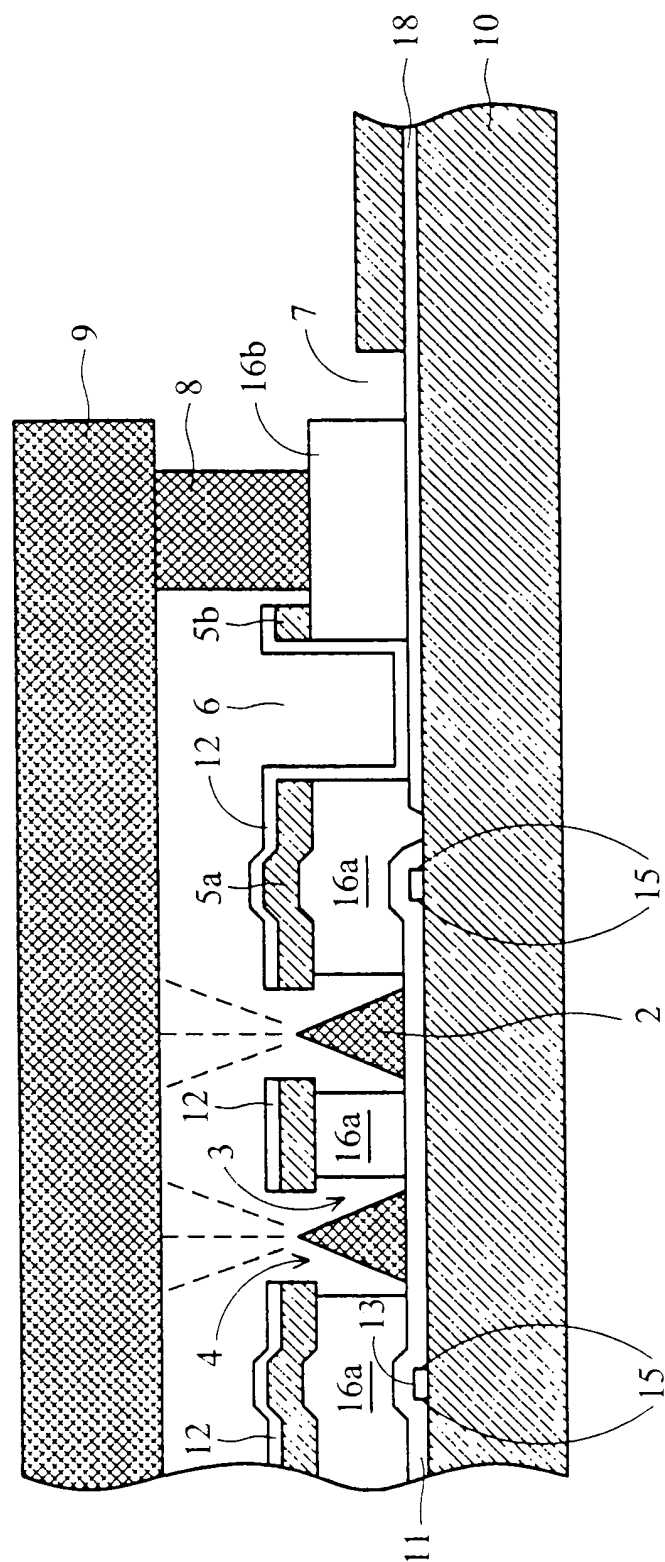


FIG. 3

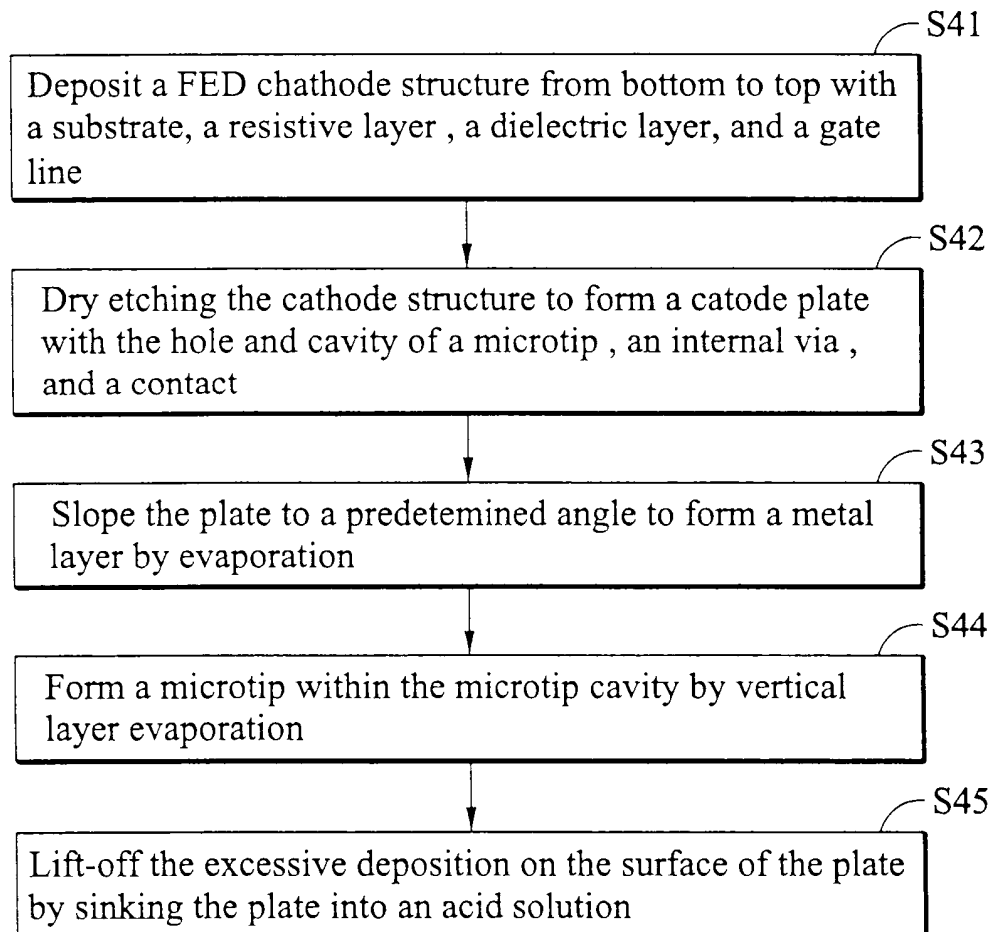


FIG. 4

Comparison	Original	Novel
layer 1	Nb: Resistor(Si): Cr: Hole: Line: Contact:	Column+Tape Line Resistive Layer Microtip Cavity+Via+Contact Gate Line
layer 2		
layer 3		
layer 4		
layer 5		
layer 6		

FIG. 5